

# Notice of Allowability

Application No.

10/780,735

Examiner

LAM T. MAI

Applicant(s)

SHIMIZU, KAZUHIRO

Art Unit

2819

*Am*

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 12/9/2005.
2. ☒ The allowed claim(s) is/are 1-26.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All b) ☐ Some\* c) ☐ None of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

## **DETAILED ACTION**

### ***Specification***

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

### ***Allowable Subject Matter***

Claims 1-26 are allowable.

The following is an examiner's statement of reasons for allowance: Claim 1 is allowed over the prior art of record. The prior art of record considered individually or in combination, fails to show or suggest claimed device comprising, among other limitations, a novel and unobvious limitation of "a voltage detecting device provided in said low potential part and configured to detect a potential at an output line of at least one of said first and second level shift parts and to supply a logic value based on the potential for said low side logic circuit, thereby controlling an operation of said low side logic circuit" structurally and functionally interconnected with other limitations in the manner as cited in claims 2-6.

Claim 7 is allowed over the prior art of record. The prior art of record considered individually or in combination, fails to show or suggest claimed device comprising, among other limitations, a novel and unobvious limitation of "a voltage detecting device provided in said high potential part and configured to detect a potential at an output line of said reverse level shift part and to supply a logic value based on said potential for

said control part, thereby causing said control part to control conduction/non-conduction of said high side switching device” structurally and functionally interconnected with other limitations in the manner as cited in claims 8-12.

Claim 13 is allowed over the prior art of record. The prior art of record considered individually or in combination, fails to show or suggest claimed device comprising, among other limitations, a novel and unobvious limitation of “a voltage detecting device provided in said low potential part and configured to detect a potential at an output line extending out of said high potential part outputting said high main power potential and to supply a logic value based on said potential for said low side logic circuit, thereby controlling an operation of said low side logic circuit” structurally and functionally interconnected with other limitations in the manner as cited in claims 14-19.

Claim 20 is allowed over the prior art of record. The prior art of record considered individually or in combination, fails to show or suggest claimed device comprising, among other limitations, a novel and unobvious limitation of “a voltage detecting device provided in said high potential part and inserted between said high main power potential and a node between said first and second switching devices, said voltage detecting device being configured to detect a potential at said node between said first and second switching devices and to supply a logic value based on said potential for said control part, thereby causing said control part to control conduction/non-conduction of said high side switching device” structurally and functionally interconnected with other limitations in the manner as cited in claims 21-26.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### **Cited References**

The prior art made of record and not replied upon is considered pertinent to application's disclosures. The cited references relate to subject matter of the instant application.

### **Conclusion**


Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAM T. MAI whose telephone number is (571)272-1807. The examiner can normally be reached on 5:30 am - 4:00pm.

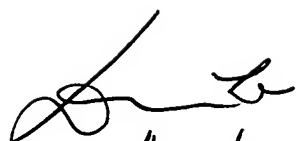
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Barnie Rexford can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Lam T. Mai   
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1/09/06

**DON LE**  
**PRIMARY EXAMINER**